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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoichi YAMADA

Application No.: 09/720,142 ✓

Filed: December 21, 2000

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Batch/Confirmation No.: 7514

Allowed: August 26, 2004

Group Art Unit: 2187

Examiner: B. Peugh

Docket No.: 107300

LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please substitute the attached 1 sheet of formal drawings depicting Figure 13 for the corresponding drawings filed with the application.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

Jude L. Cooney
Registration No. 54,045

JAO:JLC/raw

Date: September 28, 2004

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